0			2004/06/21 11:58	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	438/3 and (diffusion adj barrier)	397 .	BRS	8
0			2004/06/18 18:07	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	(438/3 and (diffusion adj barrier)) and (silicon adj nitride)	212	BRS	7
0			2004/06/21 11:58	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	438/3 and (diffusion adj barrier)	397	BRS	O
0			2004/06/18 18:06	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	145	BRS	5
0			2004/06/18 16:49	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	(438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)	418	BRS	4
0			2004/06/18 16:48	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	(438/\$.ccls. and (random adj access adj memory)) and (ferroelectric)	987	BRS	ω
0			2004/06/18 16:47	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	438/\$.ccls. and (random adj access adj memory)	9149	BRS	N
0			2004/06/18 16:46	USPAT; US-PGPUB	(("6200821") or ("6291292") or ("6355952") or ("6407422") or ("6603161")).PN.	5	IS&R	
Errors	Error Definition	Comments	Time Stamp	DBs	Search Text	Hits	Туре	

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0			2004/06/21 13:23	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	257/295 and (random adj access adj memory)	1055	BRS	16
0			2004/06/21 13:26	UB; EPO; RWENT;	(((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)) and (mergedtop adj electrodeplateline adj capacitor)	0	BRS	15
0			2004/06/21 13:17	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	145	BRS	4
0			2004/06/21 13:16	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	145	BRS	13
0			2004/06/21 13:11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	145	BRS	12
0			2004/06/21 12:03	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	(438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)	418	BRS	11
0			2004/06/21 12:02	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	438/\$.ccls. and (random adj access adj memory)	9149	BRS	10
0			2004/06/21 12:00	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	(438/3 and (diffusion adj barrier)) and (merged adj top adj electrode)	0	BRS	9
Errors	Error Definition	Comments	Time Stamp	DBs	Search Text	Hits	Туре	

20	19	18	17	
BRS	BRS	BRS	BRS 442	Туре
133	0	133	442	Hits
((257/295 and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	(((257/295 and (random adj access USPAT; adj memory)) and (ferroelectric adj US-PGPU JPO; DEF barrier)) and (merged adj electrode) IBM_TDB	((257/295 and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	(257/295 and (random adj access adj memory)) and (ferroelectric adj capacitor)	Search Text
USPAT; US-PGPUB; EPO; 2004/06/21 JPO; DERWENT; 13:27 IBM_TDB	USPAT; US-PGPUB; EPO; 2004/06/21 JPO; DERWENT; 13:25 IBM_TDB	USPAT; US-PGPUB; EPO; 2004/06/21 JPO; DERWENT; 13:27 BM_TDB	USPAT; US-PGPUB; EPO; 2004/06/21 JPO; DERWENT; 13:24 IBM_TDB	DBs
2004/06/21 13:27	2004/06/21 13:25	2004/06/21 13:27	2004/06/21 13:24	Time Stamp
				Stamp Comments
				Error Definition
0	0	0	0	Errors